

Pat 10-29  
#6

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No.: SNSY-A1996-050

Inventor(s): Narendra V. Shenoy and Lukas Van Ginneken

Serial No.: 08/886,625

Group Art Unit:

Filed: 07/01/97

Examiner:

Title: A METHOD FOR PHYSICAL PLACEMENT OF AN INTEGRATED CIRCUIT ADAPTIVE TO  
NETLIST CHANGES

The Commissioner of Patents and Trademarks

Washington, D.C. 20231

Sir:

Information Disclosure Statement Submitted Pursuant to 37 C.F.R. 1.97(b)

The citations referenced herein, copies attached, may be material to the examination of the above-identified application and are, therefore, submitted in compliance with the duty of disclosure as defined in 37 C.F.R. 1.56. The Examiner is requested to make these citations of official record in the application.

This Information Disclosure Statement submitted in accordance with 37 C.F.R. 1.97(b) is not to be construed as a representation that a search has been made, that additional items material to the examination of this application do not exist, or that any one or more of these citations constitute prior art under 35 U.S.C. 102.

The Examiner's attention is respectfully directed to the following U.S. Patents:

<u>Pat. No.</u>	<u>Pat. Title</u>	<u>Grant Date</u>
-----------------	-------------------	-------------------

The Examiner's attention is respectfully directed to the following documents:

Hojat et al.; "AN INTEGRATED PLACEMENT AND SYNTHESIS APPROACH FOR TIMING CLOSURE OF POWER PC MICROPROCESSORS"; 1997; International Conference on Computers and Processors; Austin, Texas

Murofushi et al.; "LAYOUT DRIVEN RE-SYNTHESIS FOR LOW POWER CONSUMPTION LSD's"; 1997; Semiconductor DA & TEST Engineering Center DA Development Dept., Kawasaki 210, JAPAN

Kannan et al.; "A METHODOLOGY AND ALGORITHMS FOR POST-PLACEMENT DELAY OPTIMIZATION"; 1994; Cadence Design Systems, Inc., 2655 Seely Road, San Jose, CA 95134; 31st ACM/IEEE Design Automation Conference

Stenz et al.; "TIMING DRIVEN PLACEMENT IN INTERACTION WITH NETLIST TRANSFORMATIONS"; 1997; Siemens AG Semiconductor Group 81617 Munich, Germany

Larry Stockmeyer; "OPTIMAL ORIENTATIONS OF CELLS IN SLICING FLOORPLAN DESIGNS"; 1983; Computer Science Department, IBM Research Laboratory, San Jose, CA 95193

10/26/1999 MSNIFER# 00000012 08886625

01 FC:126

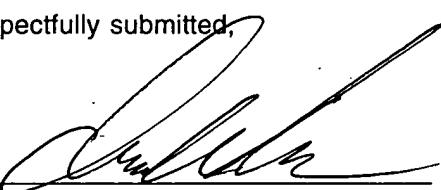
240.00 80

Please direct all correspondence concerning the above-identified application to the following address:

**WAGNER, MURABITO & HAO LLP**  
Two North Market Street, Third Floor  
San Jose, California 95113  
(408) 938-9060

Respectfully submitted,

By:

  
Anthony C. Murabito  
Reg. No. 35,295

Date: 10/15/1999

## Invention Disclosure Statement Transmittal

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Commissioner of Patents and Trademarks, Washington, D.C., 20231, on the below date of deposit.

Date of Deposit:	10/15/99	Name of Person Making the Deposit:	RODICA A. DIMA	Signature of the Person Making the Deposit:
				<i>Rodica A. Dima</i>

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Narendra V. Shenoy and Lukas Van Ginneken

Serial No.: 08/886,625 Group Art Unit:

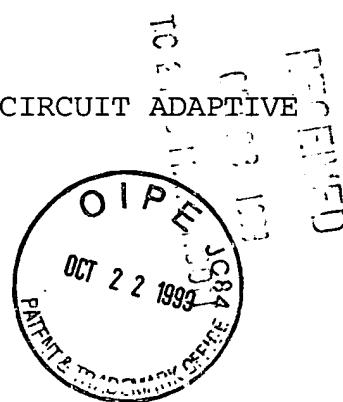
Filed: 07/01/97 Examiner:

Title: A METHOD FOR PHYSICAL PLACEMENT OF AN INTEGRATED CIRCUIT ADAPTIVE TO NETLIST CHANGES

The Commissioner of Patents and Trademarks  
Washington, D.C. 20231

Sir:

#### Information Disclosure Statement Transmittal



Transmitted herewith is the following:

Formal drawings, totaling ..... sheets.  
 Informal drawings, totaling ..... sheets.  
 Certification for PTO Consideration  
 Information Disclosure statement (\_\_\_\_ sheets)  
 Information Disclosure statement and late filing fee  
 Form 1449  
 Petition for Extension of Time  
 Other: .....

#### **Fee Calculation (for other than a small entity)**

Fee Items		Fee Rate	Total
Petition for Extension of Time (fee calculated elsewhere)		\$ 0.00	\$ 0.00
Information Disclosure Statement, late filing		\$240.00	\$240.00
Other:			\$ 0.00
<b>Total Fees</b>			<b>\$240.00</b>

#### PAYMENT OF FEES

- The full fee due in connection with this communication is provided as follows:

The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No.: 23-0085.  
 A duplicate copy of this authorization is enclosed.

A check in the amount of \$240.00

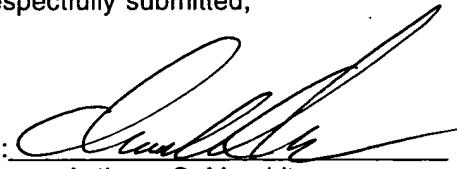
Charge any fees required or credit any overpayments associated with this filing to Deposit Account No.: 23-0085.

Please direct all correspondence concerning the above-identified application to the following address:

**WAGNER, MURABITO & HAO LLP**  
Two North Market Street, Third Floor  
San Jose, California 95113  
(408) 938-9060

Respectfully submitted,

Date: 10/15/1999

By:   
Anthony C. Murabito  
Reg. No. 35,295